# CVE Detail – CVE-2019-17391

An issue was discovered in the Espressif ESP32 mask ROM code 2016-06-08 0 through 2. Lack of anti-glitch mitigations in the first stage bootloader of the ESP32 chip allows an attacker (with physical access to the device) to read the contents of read-protected eFuses, such as flash encryption and secure boot keys, by injecting a glitch into the power supply of the chip shortly after reset.

## Threat-Mapped Scoring

Score: 0.0

Priority: Unclassified

## EPSS

EPSS Score: N/A

Percentile: 0.5459

## CVSS Scoring

CVSS v3.1 Score: 4.6

Severity: MEDIUM

## Mapped CWE(s)

* CWE-755: Improper Handling of Exceptional Conditions

## Affected Products

* cpe:2.3:o:espressif:esp32-d0wd\_firmware:-:\*:\*:\*:\*:\*:\*:\*
* cpe:2.3:o:espressif:esp32-d2wd\_firmware:-:\*:\*:\*:\*:\*:\*:\*
* cpe:2.3:o:espressif:esp32-s0wd\_firmware:-:\*:\*:\*:\*:\*:\*:\*
* cpe:2.3:o:espressif:esp32-pico-d4\_firmware:-:\*:\*:\*:\*:\*:\*:\*